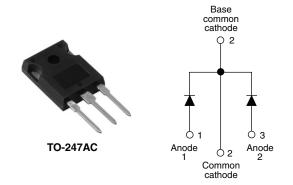


Vishay High Power Products

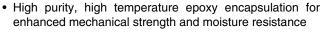
Schottky Rectifier, 2 x 30 A



PRODUCT SUMMARY				
I _{F(AV)}	2 x 30 A			
V _R	150 V			

FEATURES

- 175 °C T_J operation
- Center tap TO-247 package
- Low forward voltage drop
- · High frequency operation



- Guard ring for enhanced ruggedness and long term reliability
- Lead (Pb)-free ("PbF" suffix)
- Designed and qualified for industrial level

DESCRIPTION

The 60CPQ150PbF center tap Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS				
SYMBOL CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	60	Α	
V _{RRM}		150	V	
I _{FSM}	t _p = 5 μs sine	2300	A	
V _F	30 Apk, T _J = 125 °C (per leg)	0.67	V	
T _J	Range	- 55 to 175	°C	

VOLTAGE RATINGS					
PARAMETER	SYMBOL	60CPQ150PbF	UNITS		
Maximum DC reverse voltage	V_{R}	150	V		
Maximum working peak reverse voltage	V_{RWM}	150 V			

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	L TEST CONDITIONS		VALUES	UNITS
Maximum average per leg forward current			50 % duty cycle at T _C = 151 °C, rectangular waveform		30	
See fig. 5	per device	'F(AV)	$I_{F(AV)}$ 50 % duty cycle at $I_C = 151$ °C,	, rectangular wavelonn	60	Α
Maximum peak one cycle non-repetitive surge current per leg See fig. 7			5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated V _{RRM} applied	2300	A
		IFSM	10 ms sine or 6 ms rect. pulse		510	
Non-repetitive avalanche energy per leg		E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 1 mH		0.5	mJ
Repetitive avalanche current per leg		I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		1	Α

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

60CPQ150PbF

Vishay High Power Products Schottky Rectifier, 2 x 30 A



ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS		TYP.	MAX.	UNITS	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	30 A	T _J = 25 °C	0.80	0.83	V	
		60 A		0.93	0.99		
		30 A	T _J = 125 °C	0.64	0.67		
		60 A		0.74	0.77		
Maximum reverse leakage current per leg			V _B = Rated V _B	10	100	μΑ	
See fig. 2	I _{RM}	T _J = 125 °C	VR = nateu VR	12	25	mA	
Typical junction capacitance per leg	C _T	V _R = 5 V _{DC} (test signal range 100 kHz to 1 MHz) 25 °C		-	820	pF	
Typical series inductance per leg	L _S	Measured lead to lead 5 mm from package body		-	7.5	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R - 1		10 000	V/µs		

Note

 $^{^{(1)}}$ Pulse width < 300 μ s, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range)	T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		В	DC operation See fig. 4		
Maximum thermal resistance, junction to case per package		□ thJC	R _{thJC} DC operation		°C/W
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.25	
Approximate weight				6	g
				0.21	OZ.
Manustina taunus	minimum			6 (5)	kgf ⋅ cm
Mounting torque -	maximum			12 (10)	(lbf · in)
Marking device Case style TO-247AC (JEDEC) 60CPQ		Q150			

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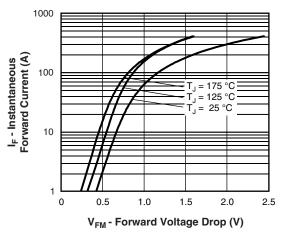


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

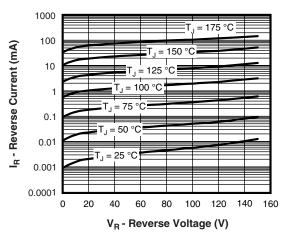


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)

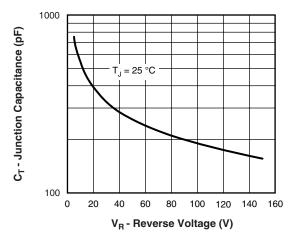


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

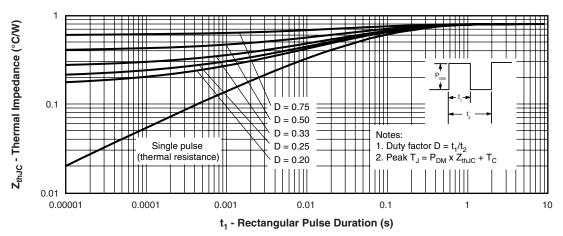


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

Vishay High Power Products Schottky Rectifier, 2 x 30 A



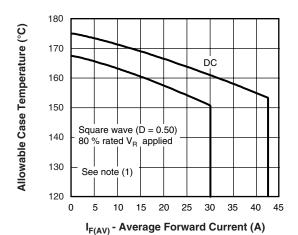


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

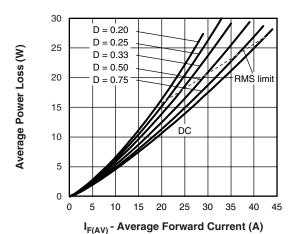
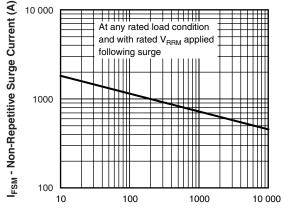


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

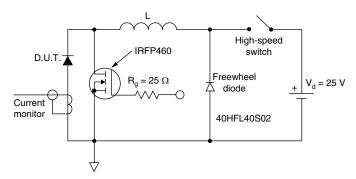


Fig. 8 - Unclamped Inductive Test Circuit

Note

 $^{(1)}$ Formula used: T_C = T_J - (Pd + Pd_{REV}) x R_{thJC}; Pd = Forward power loss = I_{F(AV)} x V_{FM} at (I_{F(AV)}/D) (see fig. 6); Pd_{REV} = Inverse power loss = V_{R1} x I_R (1 - D); I_R at V_{R1} = 80 % rated V_R

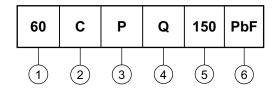
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ORDERING INFORMATION TABLE

Device code



1 - Current rating (60 = 60 A)

2 - Circuit configuration:

C = Common cathode

Package:

P = TO-247

4 - Schottky "Q" series

5 - Voltage code (150 = 150 V)

6 - • None = Standard production

• PbF = Lead (Pb)-free

Tube standard pack quantity: 25 pieces

LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95223					
Part marking information	http://www.vishay.com/doc?95226				

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